

Registration form

One week workshop

On

Introduction to PLC and SCADA

1. Name Mr./Ms :
2. Branch & Year:
3. Institution Address:

4. Phone:
5. E-mail:
6. Detail of fee Amount:
7. ISTE Membership no:

I declare that the details furnished above are correct to the best of my knowledge and belief. I also undertake to abide by the rules and other condition prescribed to conduct the training programme at C.K.Pithawala College of Engg. & Tech., Surat.

Place:

Date: (Signature) _____

Permission Certificate

Mr. /Ms.. _____ of the Dept. of _____ is permitted / sponsored to attend two days workshop on **Introduction to PLC and SCADA** and will be permitted to attend the same during the aforesaid period.

Place:

Date: (Signature) _____

Head of the department / Institution

Fee Receipt:

Mr. /Ms. _____ has submitted fees of an amount _____ to attend workshop on **Introduction to PLC and SCADA**.

Date:

Received by: _____

Workshop Contents

Fundamentals of PLC and SCADA:

- Basics of PLC
- PLC programming
- Interfacing of I/O devices
- Interfacing of relay
- Demo of industrial process
- Introduction to SCADA
- Laboratory Session

Hands on Session

- On interfacing of input/output devices
- Ladder logic programming
- On debugging programming

Important Dates:

- Receiving application: 20/01/2010
- Notification of Acceptance : 25/01/2010
- Program Dates: 27th to 31st January, 2010

Address for Correspondence:

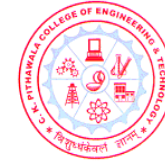
- Mr. Kapil K. Patel (M: 9825368637)
- Mrs. Amisha H. Kapadia (M: 9879775276)

E-mails:

- kkapatel_24@yahoo.co.in

Committee Member:

- Mr. Milind B.Trivedi
- Mrs. Jyoti P. Shah
- Ms. Prachi J. Limdi
- Mrs. Arpita V. Shah



ISTE Approved one week workshop On “Introduction to PLC and SCADA”

27th to 31st January 2010

Coordinated by:

Prof. Naimish Zaveri

Assistant Professor, Department of Electrical Engineering

Prof. Tejas Zaveri

Assistant Professor & I/C HOD, Department of Electrical Engineering

Co-coordinators

Mr. Kapil K. Patel

Lecturer, Department of Electrical Engineering

Mrs. Amisha H. Kapadia

Lecturer, Department of Electrical Engineering

Organized by:

**Electrical Engineering Department
C.K.Pithawala College of Engineering &
Technology**

Near Malvan Mandir, Via Magdalla Port,
Dumas Road, Surat-395007.

Website: www.ckpcet.ac.in

Contact No: 0261- 2728282

About Host Institute

C. K. Pithawalla College of Engineering and Technology is a self financed Institute. The institute becomes functional in December, 1998 with affiliation to Veer Narmad South Gujarat University, Surat. C.K Pithawalla College of Engineering & Technology is being managed by the Navyug Vidyabhavan Trust which was founded in February, 1965. The college has provided all the necessary infrastructure and facilities to help the students along with a well established library, laboratories with containing modern facilities to provide better education in technology.

Our Mission

Is to transform C. K. Pithawalla Educational Complex in a leading Vishwa Vidyalaya as a premier seat of higher learning and research. The task is quite challenging but we decided to accept this challenge of bringing this Pithawalla Educational Complex as an eminent center on the academic map of the country.

About ISTE Gujarat Section

The Indian Society for Technical Education is a national, professional, non-profit making Society registered under the Societies Registration Act of 1860. First started in 1941 as the Association of Principals of Technical Institutions (APTI), it was converted into "Indian Society for Technical Education" in 1968 with a view to enlarge its activities to advance the cause of technological education.

The major objective of the ISTE is to assist and contribute in the production and development of top quality professional engineers and technicians needed by the industries and other organizations.

Schedule of Workshop on "Introduction To PLC and SCADA"

Date: 27/01/2010

10:00am to 11:00 am	Inauguration
11:00am to 1:00pm	Lecture-I(Basic PLC Theory)
1:00 pm to 2:00 pm	Lunch Break
2:00 pm to 3:30 pm	Lab session-I (PLC)
3:30pm to 3:45 pm	Tea Break
3:45 pm to 5:00 pm	Lab session-II (PLC)

Date: 28/01/2010

11:00am to 1:00pm	Lab session-I (PLC)
1:00 pm to 2:00 pm	Lunch Break
2:00 pm to 3:30 pm	Lab session-II (PLC)
3:30pm to 3:45 pm	Tea Break
3:45 pm to 5:00 pm	Lab session-III (PLC)

Date: 29/01/2010

11:00am to 1:00pm	Lab session-I (PLC)
1:00 pm to 2:00 pm	Lunch Break
2:00 pm to 3:30 pm	Lab session-II (PLC)
3:30pm to 3:45 pm	Tea Break
3:45 pm to 5:00 pm	Lab session-III (PLC)

Date: 30/01/2010

11:00am to 1:00pm	Lecture-I (SCADA Theory)
1:00 pm to 2:00 pm	Lunch Break
2:00 pm to 3:30 pm	Lab session-I (SCADA)
3:30pm to 3:45 pm	Tea Break
3:45 pm to 5:00 pm	Lab session-II (SCADA)

Date: 31/01/2010

11:00am to 1:00pm	Lab session-I (SCADA)
1:00 pm to 2:00 pm	Lunch Break
2:00 pm to 3:30 pm	Lab session-II (SCADA)
3:30pm to 3:45 pm	Tea Break
3:45 pm to 4:45 pm	PLC & SCADA (test)
5:00pm to 5:30pm	Valedictory Function

Course Registration fee (non-refundable):

Rs. 850/- Students

Rs.700/- ISTE member Student

Registration:

The participant must be final/ pre final year student of AICTE approved engineering institution. Please include your contact e-mail, phone number, and address.

Payments should be made in cash. The applicants must also send an e-mail to coordinators and express their intent to participate in the program.

Advisory Committee:

Dr. Anish Gandhi

(Asst.Professor & I/C HOD , Mech Dept., CKPCET)

Prof.Priti Patel

(Asst.Professor & I/C HOD, Civil Dept., CKPCET)

Prof. Ami Choksi

(Asst.Professor & I/C HOD , Comp Dept., CKPCET)

Prof. Reena Popawala

(Asst.Professor, Civil Dept &
Chief Convener ISTE Chapter, CKPCET)

Prof. Ninad Bhatt

(Asst.Professor & I/C HOD ,ECC Dept., CKPCET)